

TE/SEM V / Comp / CBSOS / EN

23/05/2016

QP Code : 31134

(3 Hours)

Total marks : 80

Note:

- Question No. 1 is compulsory.
- Attempt any Three questions out of remaining questions.
- Make suitable assumptions whenever necessary.

Q 1: [4 X 5]

- Compare connection oriented and connectionless services.
- Explain in short Subnetting.
- Explain in short different framing Methods.
- Explain in short TCP/IP Model.
- What is the use of SSH ?

Q 2: [10]
a) Explain any four functions of Data Link layer with example. [10]
b) What is IPv4 protocol? Explain the IPv4 Header format with diagram. [10]

Q 3: [10]
a) Explain Classless Inter Domain Routing (CIDR). [10]
b) Discuss the quality of service parameters in computer network. [10]

Q 4: [10]
a) What are the steps involved in link state routing. Explain the contents and requirements of link state packets. [10]
b) Compare Open Loop congestion control, Closed Loop congestion control. [10]

Q 5: [10]
a) Write a Program for client-server application using Socket Programming(TCP). [10]
b) An ISP is granted a block of addresses starting with 150.80.0.0/16. The ISP wants to distribute these blocks to 2600 customers as follows.
a. The first group has 200 medium-size businesses; each needs 128
b. The second group has 400 small businesses; each needs 16
c. The third group has 2000 households; each needs 4 addresses. Design the subblocks and give the slash notation for each subblock. Find out how many addresses are still available after these allocations. [10]

Q 6: Write short notes on the following. [5 X 4]

- Virtual LAN
- FDDI
- BGP
- SNMP

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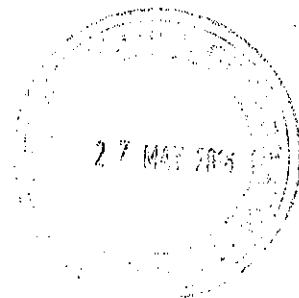
27/05/2016

(3 HOURS)

[Total Marks: 80]

- N.B.: (1) Question no. 1 is compulsory.
(2) Attempt any three questions from remaining.
(3) Assume suitable data wherever necessary.

- Q1. (a) What is UML? Explain role of different UML diagrams in system design. (10)
(b) Explain DFD (up to two levels) with suitable example. (10)
- Q2. (a) Explain different requirement gathering techniques used in system analysis. (10)
(b) Explain software development life cycle used for system analysis. (10)
- Q3. (a) What is cost benefit analysis? Illustrate any one model of cost benefit analysis. (10)
(b) Explain Zachman's framework for software system design. (10)
- Q4.(a) Draw the use case diagram of a library management system with extend, include and generalize relations between use cases. (10)
(b) What are characteristics of good graphical user interface? Draw GUI for online course registration system. (10)
- Q5.(a) Assume that the hospital management system is deployed using 3 tier architecture, explain its various components and its deployment with the help of diagrams. (10)
(b) Draw the sequence diagram for login procedure to a system. Include all possible scenarios and draw its activity diagram also. (10)
- Q6. (a) Define cohesion and coupling in system design? High cohesion and low coupling is recommended for good system design. Justify. (10)
(b) Your college wishes to prepare and maintain the database system to track progress of the students who were recruited through college training and placement cell. Prepare the proposal to design above system. (10)



TE/SEM V/ comp/ CBS 45/ Microprocessor
17/05/2016

QP Code : 31091

(80 Marks) (3 Hours)

- Question no. 1 is compulsory.
- Answer any three questions from question no. 2 – 6.
- Assume suitable data, if necessary.

- Q.1. Answer following questions in brief.
- Explain programming model of 8086. (05)
 - Explain V86 mode of 80386DX. (05)
 - Explain, in brief, pipeline stages on Pentium processor. (05)
 - Explain, in brief, data format supported by SuperSparc processor. (05)
- Q.2. a. Explain memory segmentation with pros and cons. (08)
b. Draw and explain the block diagram of 8255. Also, explain different operating modes of 8255. (12)
- Q.3. a. Design 8086 based minimum mode system for following requirements: (12)
i. 256 KB of RAM using 64 KB x 8-bit device
ii. 128 KB of RAM using 64 KB x 8-bit device
iii. Three 8-bit parallel ports using 8255
iv. Support for 8 interrupts
- b. Explain, in brief, cache organization of Pentium processor. (08)
- Q.4. a. Draw and explain architecture of SuperSparc processor. (12)
b. Discuss, in brief, protection mechanism of 80386DX. (08)
- Q.5. a. Draw and explain architecture of Pentium processor. (10)
b. Draw timing diagram of read operation on 8086 based system. (10)
- Q.6. Write short notes on (05)
a. 8089 I/O Processor (05)
b. Comparison between i5 and i7 (05)
c. SuperSparc registers (05)
d. 8259 – PIC (05)



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